

[illegible]

FIG. 2

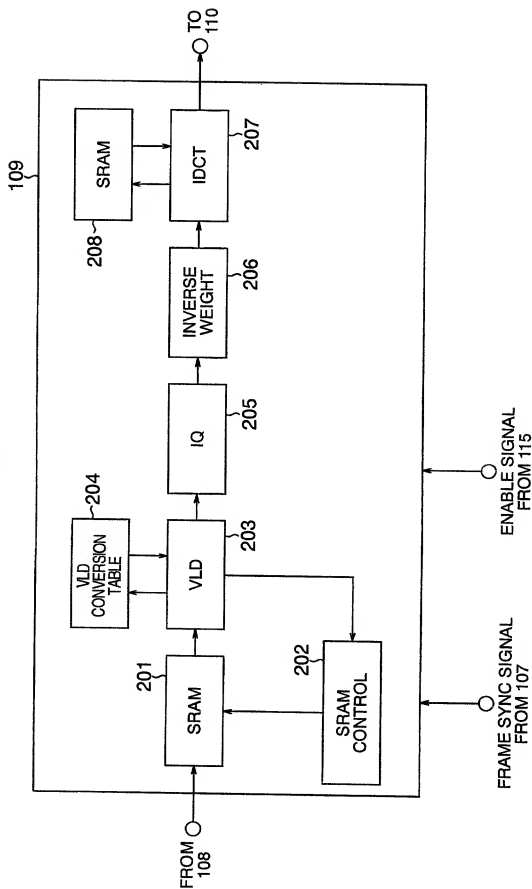


FIG. 3

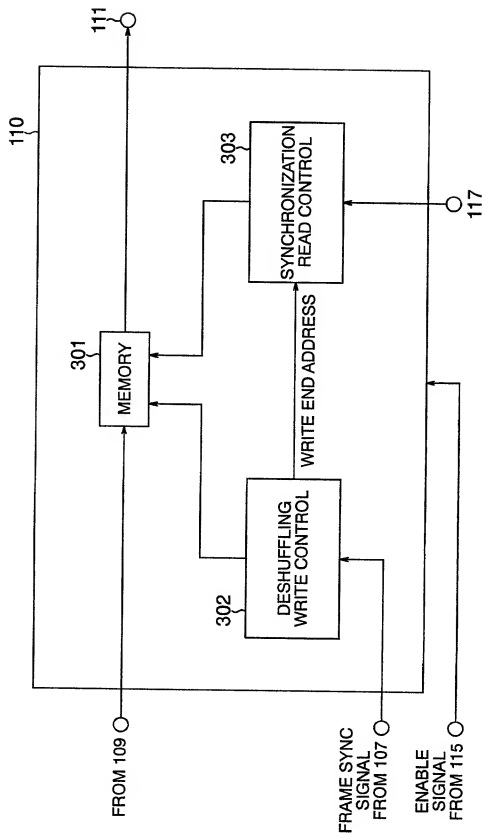


FIG. 4

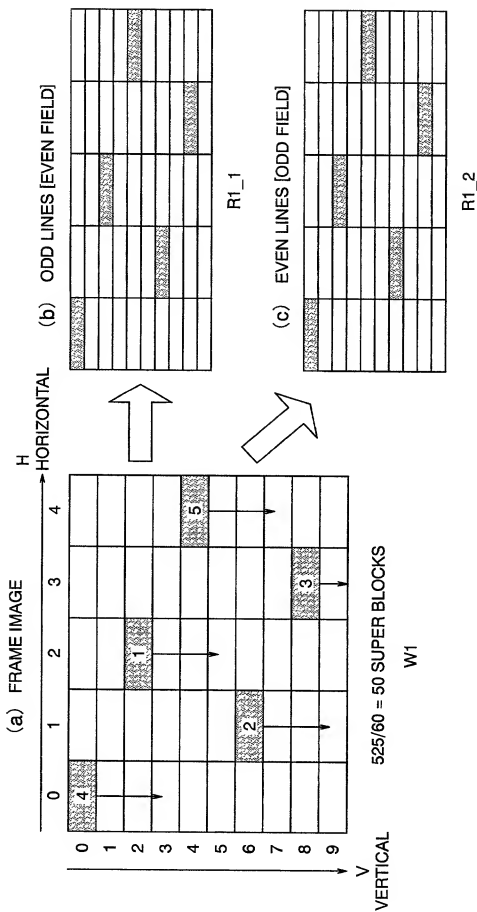


FIG. 5

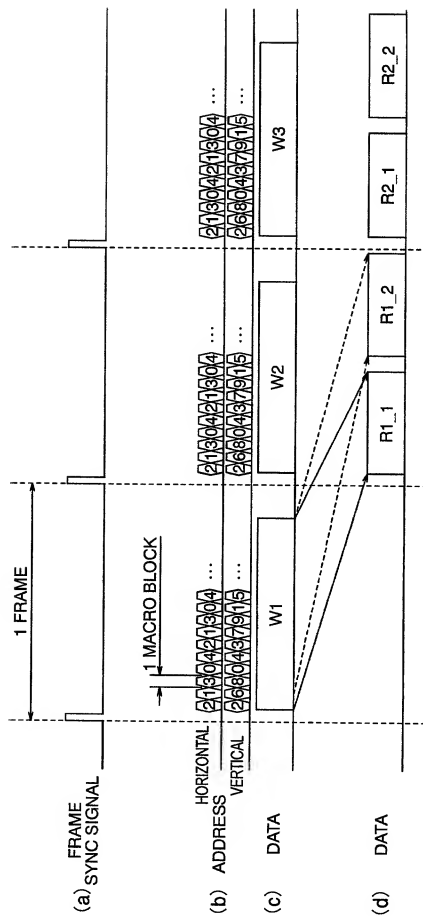


FIG. 6A

WHEN THE FRAME SYNC SIGNAL FOR OUTPUT IS
[EARLIER] THAN THE INPUT SIDE FRAME SYNC SIGNAL

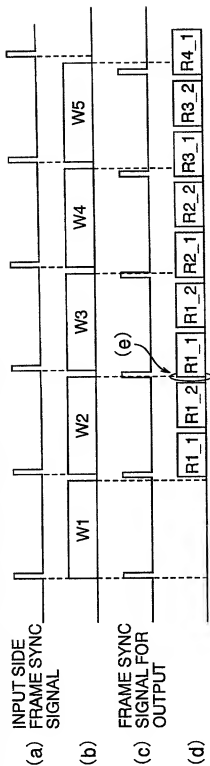


FIG. 6B

WHEN THE FRAME SYNC SIGNAL FOR OUTPUT IS
[LATER] THAN THE INPUT SIDE FRAME SYNC SIGNAL

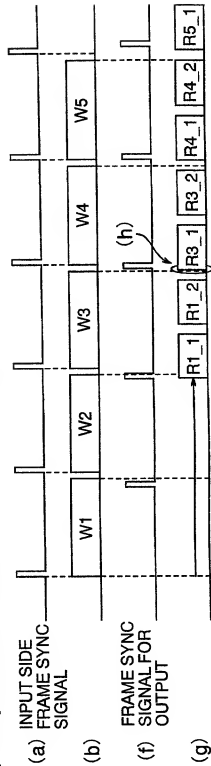


FIG. 7

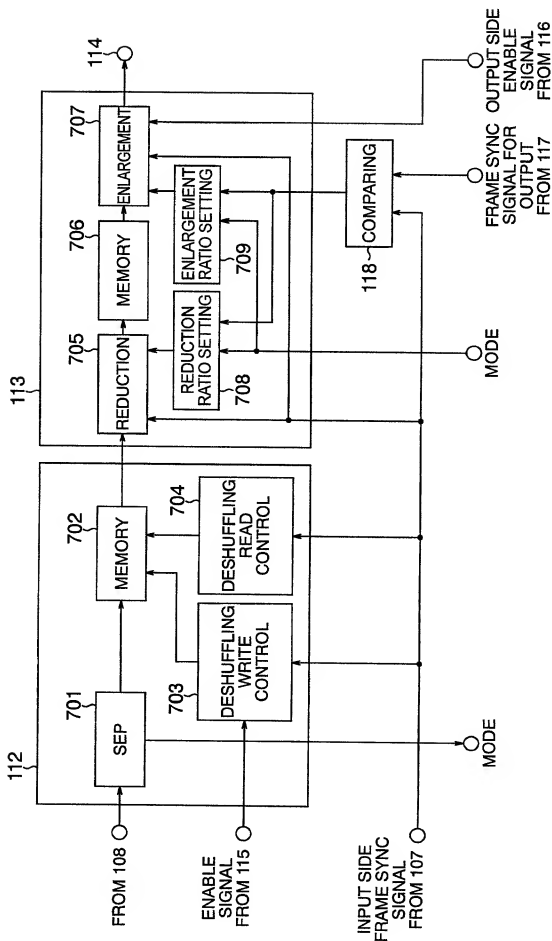


FIG. 8

UNLOCKED MODE					LOCKED MODE
MODE	SAMPLES / FRAME			AVERAGE	
	MAXIMUM	MINIMUM			
525/60 SYSTEM	48KHz	1620	1580	1601.60	1st FRAME :1600 2nd TO 5th FRAME :1602
	44.1KHz	1489	1452	1471.47	
	32KHz	1080	1053	1067.73	1st AND 8th FRAME :1066 2~7, 9~15 FRAME :1068
	32KHz 2ch	1080	1053	1067.73	
625/50 SYSTEM	48KHz	1944	1896	1920	ALL FRAME :1920
	44.1KHz	1786	1742	1764	
	32KHz	1296	1264	1280	ALL FRAME :1280
	32KHz 2ch	1296	1264	1280	

FIG. 9

WHEN THE FRAME SYNC SIGNAL FOR OUTPUT IS
[EARLIER] THAN THE INPUT SIDE FRAME SYNC SIGNAL

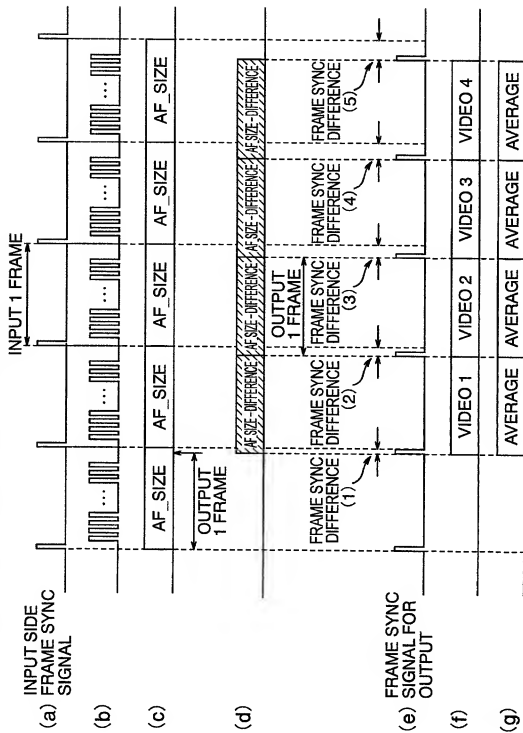


FIG. 10

WHEN THE FRAME SYNC SIGNAL FOR OUTPUT IS
[LATER] THAN THE INPUT SIDE FRAME SYNC SIGNAL

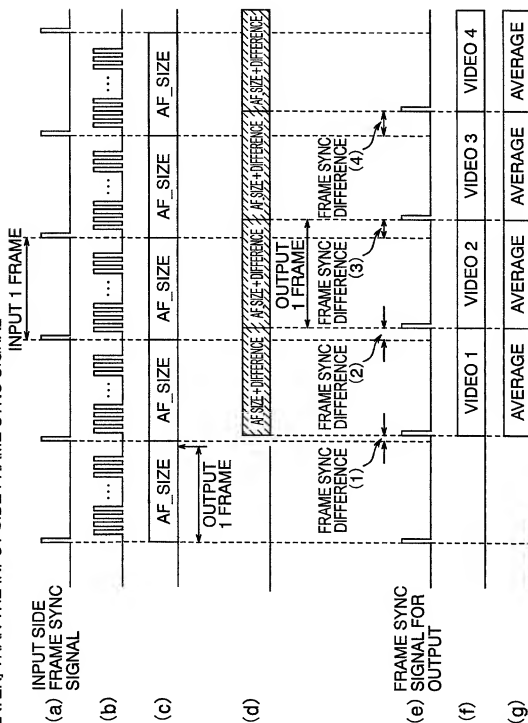


FIG. 11

